

# RDS-TCB TAIL CIRCUIT BUFFER



## FEATURES / BENEFITS

- √ Interface Modules Available for - V.35 RS-422/449, RS-530, RS-232 or X.21
- √ User Selectable buffer sizes of 1,024, 2,048, 4,096 and 8,192 bits
- √ Data Rates - Synchronous to 2.048Mbps
- √ Timing - accepts external timing from attached DCE
- √ Rackmount or Standalone
- √ 110/220VAC switch selectable

## DESCRIPTION

The RDS-TCB *Tail-Circuit Buffer* is designed to provide selectable bi-directional buffering between two data circuits that are operating at nominally the same clock rate and are capable of providing clocking as a DCE. In such cases, the timing of the two circuits is not locked to the same timing source, or may be allowed to deviate from a common timing source for a length of time. The RDS-TCB meets this need by providing selectable amounts of bi-directional memory from 1,024 bits up to 8,192 bits and supports synchronous clock rates up to 2.048 Mbps.

The data memory is based upon a self-centering FIFO buffer. Upon power-on, a reset, or a prior buffer underrun or overflow, the buffer will accept and hold all input data until the buffer is half full. At that point, the buffer will be released to output the first data received and will continue input and output through the FIFO at the rate determined by the input and output clocks. Differences in clocking rates on the two interfaces will result in the amount of data queued in the buffer changing over time. As long as the amount queued does not decrease to zero (FIFO empty) or reach the maximum memory value selected (FIFO full), then data output will correctly match data input, with a delay that is a function of the length of the queue and the bit clock rate.

An additional option controls whether certain control signals, namely DCD on the input side to RTS on the output side, are passed directly through the buffer without delay, or through the buffer memory with the same delay as data.

The RDS-TCB is housed in a sturdy metal standalone or Rackmount enclosure and operates on 110/220VAC.

The unit has a three-year warranty and a 24-hour turnaround on warranty repairs.

**EAST COAST DATACOM, INC.**

# SPECIFICATIONS

## Application

Elastic data buffering (FIFO memory) function between two systems having nominally equivalent clocking rates

## Buffer Size/Function

User Selectable: 1024, 2048, 4096 and 8192 bits

## Control Signal Options

User selectable DCD to RTS buffer bypass or enable

## Capacity

Two (2) DCE devices, one for each port

## Data Rates

Up to 2.048Mbps

## Data Channel Interfaces

Available in V.35, RS-530, RS-422/449, RS-232 or X.21

## Surge Protection

Main power supply

## Power Source

100-120 to 200-220VAC @10%, 50/60Hz, 0.16/0.08A, external 110/220 volt select switch, IEC Power Inlet, (2) 5mm Fuses

## Environmental

Operating Temperature....32° to 122° F (0° to 50° C)  
Relative Humidity.....5 to 95% Non-Condensing  
Altitude.....0 to 10,000 feet

## Dimensions

Height ..... 1.75 inches (4.44 cm)  
Width ..... 17.00 inches (43.18 cm)  
Length ..... 9.00 inches (22.86 cm)

## Weight

2 pounds (0.914Kg)

## Warranty

Three Years, Return To Factory

## ORDERING INFORMATION

Part Number: 160000

Model: RDS-TCB

Description: Tail Circuit Buffer

Interfaces Available:

V.35, PT# 129010

RS-530, PT# 129011

RS-422/449, PT# 129012

X.21, PT# 129013

RS-232, PT# 129014

## INCLUDED WITH EACH UNIT:

- 1) Operations Manual
- 2) U.S.A. Grounded Power Cord, Part # 713015
- 3) Optional Power Cords
  - A) United Kingdom, Part # 713016
  - B) Continental Europe, Part # 713017
  - C) Other: Specify Country on Purchase Order

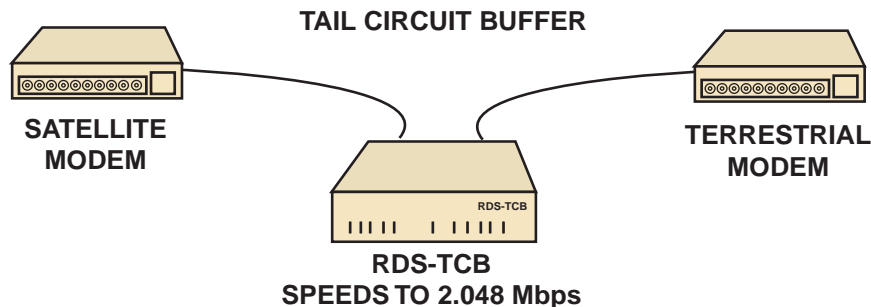
## OPTIONAL ACCESSORIES

- 1) Spare Data Center Fuses
  - A) 160ma Fuse, Qty (2) Part # 714000
  - B) 80ma Fuse, Qty (2) Part # 714001

For further detailed technical information on this product, contact East Coast Datacom Technical Assistance toll free at (800) 240-7948

## OTHER EAST COAST DATACOM PRODUCTS

MODEM AND PORT SHARING DEVICES  
INTERFACE CONVERTERS  
CAMPUS MODEM



SPECIFICATIONS ARE SUBJECT TO CHANGE WITHOUT NOTICE

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